

100-699466 PTO  
09/699466  
438 487

438	Class	487	Subclass
ISSUE CLASSIFICATION			

PATENT NUMBER

Post Available Copy

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED A.C. Q.A. 9/11/91

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/699466	D F	438		2829	SAKAR

APPLICANTS  
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TITLE  
Method for fabricating a semiconductor device

PTO-2040  
12/89

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
438	487	438	149	164	479	482	486
INTERNATIONAL CLASSIFICATION							
H01L	21/20						
H01L	21/36						

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/>	7	34	GF & 5E	30	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	ASSISTANT EXAMINER: KUMAR SARKAR 9/22/03 (Assistant Examiner) (Signature) Date:			NOTICE OF ALLOWANCE MAILED <i>09/24/03</i>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	KAMAL GUPTA SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 (Primary Examiner) (Signature) Date:			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) (Signature) Date: 9/24/03			Amount Due: \$1,300.00	Date Paid:
ISSUE BATCH NUMBER <i>9/24/03</i>					

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Formal Drawings ( *smt, set* )

(FACE)